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[Hardware/software codesign and system synthesis, 2006. CODES+ISSS '06. Proceedings of the 4th international conference 22-25 Oct. 2006](#) Page(s): 229 - 234
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[Computer Design, 2003. Proceedings, 21st International Conference on 13-15 Oct. 2003](#) Page(s): 474 - 480
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Park, C.; Jaeyu Seo; Sunghwan Bae; Hyojun Kim; Shinhan Kim; Bumsoo Kim;
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5. The Implementation of a Hybrid-Execute-In-Place Architecture to Reduce the Embedded System Memory Footprint and Minimize Boot Time

Benavides, Tony; Treon, Justin; Hulbert, Jared; Chang, Willie;

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6. The Apparatus and Enabling of a Code Balanced System

Benavides, Tony; Treon, Justin; Chang, Weide;

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